


## Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Yokogawa signal name
Module	Channel	IS terminals TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector CN1, CN2	SDV531
1	1	1	5a	8a	48	OUT1+
		4	5b	7a	47	com1
		2	1a	10a		n.c.
		5	1b	9a		n.c.
2	2	1	5a	8a	46	OUT2+
		4	5b	7a	45	com2
		2	1a	10a		n.c.
		5	1b	9a		n.c.
3	3	1	5a	8a	44	OUT3+
		4	5b	7a	43	com3
		2	1a	10a		n.c.
		5	1b	9a		n.c.
4	4	1	5a	8a	42	OUT4+
		4	5b	7a	41	com4
		2	1a	10a		n.c.
		5	1b	9a		n.c.
5	5	1	5a	8a	40	OUT5+
		4	5b	7a	39	com5
		2	1a	10a		n.c.
		5	1b	9a		n.c.
6	6	1	5a	8a	38	OUT6+
		4	5b	7a	37	com6
		2	1a	10a		n.c.
		5	1b	9a		n.c.
7	7	1	5a	8a	36	OUT7+
		4	5b	7a	35	com7
		2	1a	10a		n.c.
		5	1b	9a		n.c.
8	8	1	5a	8a	34	OUT8+
		4	5b	7a	33	com8
		2	1a	10a		n.c.
		5	1b	9a		n.c.
					9, 11 ... 16, 49	COM
					1, 50	CBSE
					2 ... 8, 10	24 V
					17 ... 32	n.c.

Terminal pin-out		
Power supply	X20	3+
		4-
	X20	5+
		6-
Potential-free fault indication output	X20	1, 2

 The loop drawing has to be observed. For information see corresponding data sheet on [www.pepperl-fuchs.com](http://www.pepperl-fuchs.com).