

Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Triconex signal name
Module	Channel	IS terminals TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	3604E, 3625, 3664
1	1	1	5a	8b	KK	LOAD1
		4	5b			
	2	2	1a	9b	DD	LOAD2
		5	1b			
2	3	1	5a	8b	u1	LOAD3
		4	5b			
	4	2	1a	9b	k1	LOAD4
		5	1b			
3	5	1	5a	8b	a1	LOAD5
		4	5b			
	6	2	1a	9b	R	LOAD6
		5	1b			
4	7	1	5a	8b	E	LOAD7
		4	5b			
	8	2	1a	9b	A	LOAD8
		5	1b			
5	9	1	5a	8b	NN	LOAD9
		4	5b			
	10	2	1a	9b	JJ	LOAD10
		5	1b			
6	11	1	5a	8b	y1	LOAD11
		4	5b			
	12	2	1a	9b	n1	LOAD12
		5	1b			
7	13	1	5a	8b	d1	LOAD13
		4	5b			
	14	2	1a	9b	V	LOAD14
		5	1b			
8	15	1	5a	8b	K	LOAD15
		4	5b			
	16	2	1a	9b	D	LOAD16
		5	1b			
					LL, EE, v1, l1, b1 S, F, B, MM, HH x1, m1, c1, U, J, C	RTN
					AA, z1, p1, h1, e1, W W, L, M, BB, CC t1, j1, f1, Z, P, N	V _{cc}

Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Triconex signal name
Module	Channel	IS terminals TB2	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X2	3604E, 3625, 3664
9	17	1	5a	8b	KK	LOAD17
		4	5b			
	18	2	1a	9b	DD	LOAD18
		5	1b			
10	19	1	5a	8b	u1	LOAD19
		4	5b			
	20	2	1a	9b	k1	LOAD20
		5	1b			
11	21	1	5a	8b	a1	LOAD21
		4	5b			
	22	2	1a	9b	R	LOAD22
		5	1b			
12	23	1	5a	8b	E	LOAD23
		4	5b			
	24	2	1a	9b	A	LOAD24
		5	1b			
13	25	1	5a	8b	NN	LOAD25
		4	5b			
	26	2	1a	9b	JJ	LOAD26
		5	1b			
14	27	1	5a	8b	y1	LOAD27
		4	5b			
	28	2	1a	9b	n1	LOAD28
		5	1b			
15	29	1	5a	8b	d1	LOAD29
		4	5b			
	30	2	1a	9b	V	LOAD30
		5	1b			
16	31	1	5a	8b	K	LOAD31
		4	5b			
	32	2	1a	9b	D	LOAD32
		5	1b			
					LL, EE, v1, l1, b1 S, F, B, MM, HH x1, m1, c1, U, J, C	RTN
					AA, z1, p1, h1, e1, W W, L, M, BB, CC t1, j1, f1, Z, P, N	V _{cc}

Terminal pin-out		
Power supply	X20	1+
		1-
	X20	2+
		2-
ERR	X20	Fault Bus

Module pin-out (SL1): module 1 ... 16	
V _{cc}	2a
	2b
GND	1a
	1b
	7a, 7b
	9a, 10b
ERR	6b



The loop drawing has to be observed. For information see corresponding data sheet on www.pepperl-fuchs.com.