Pin-Out Table

Termination Board field side			Modules		Termination Board control side				Emerson signal name
Module	Channel	IS terminals TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector CN1	System connector CN2	System connector CN3	System connector CN4	DeltaV
1	1	1	5a	7a	1		1		CH1-
		4	5b	8a	2		2		CH1+
	2	2	1a	9a	3		3		CH2-
		5	1b	10a	4		4		CH2+
	3	3	3a	10b	5		5		CH3-
		6	3b	9b	6		6		CH3+
	4	7	7a	7b	7		7		CH4-
		8	7b	8b	8		8		CH4+
	Screen	9							
2	1	1	5a	7a	9		9		CH5-
		4	5b	8a	10		10		CH5+
	2	2	1a	9a	11		11		CH6-
		5	1b	10a	12		12		CH6+
	3	3	3a	10b	13		13		CH7-
		6	3b	9b	14		14		CH7+
	4	7	7a	7b	15		15		CH8-
		8	7b	8b	16		16		CH8+
	Screen	9							
3	1	1	5a	7a		1		1	CH1-/CH9-
		4	5b	8a		2		2	CH1+/CH9+
	2	2	1a	9a		3		3	CH2-/CH10-
		5	1b	10a		4		4	CH2+/CH10+
	3	3	3a	10b		5		5	CH3-/CH11-
		6	3b	9b		6		6	CH3+/CH11+
	4	7	7a	7b		7		7	CH4-/CH12-
		8	7b	8b		8		8	CH4+/CH12+
	Screen	9							
4	1	1	5a	7a		9		9	CH5-/CH13-
		4	5b	8a		10		10	CH5+/CH13+
	2	2	1a	9a		11		11	CH6-/CH14-
		5	1b	10a		12		12	CH6+/CH14+
	3	3	3a	10b		13		13	CH7-/CH15-
		6	3b	9b		14		14	CH7+/CH15+
	4	7	7a	7b		15		15	CH8-/CH16-
		8	7b	8b		16		16	CH8+/CH16+
	Screen	9							
							17 … 24	17 … 24	n.c.

Terminal pin-out		
Power supply	X20	1+
		2-
	X20	3+
		4-

Module pin-out (SL1): module 1 ... 4	
V_{cc}	2a
	2b
GND	1a
	1b

The loop drawing has to be observed. For information see corresponding data sheet on www.pepperl-fuchs.com.

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