

Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Emerson signal name
Module	Channel	IS terminals TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector CN1	DeltaV
1	1	1	5a	7a	1	CH1-
		4	5b	8a	18, 20	Return
	2	2	1a	9a	2	CH2-
		5	1b	10a	18, 20	Return
	3	3	3a	10b	3	CH3-
		6	3b	9b	18, 20	Return
	4	7	7a	7b	4	CH4-
		8	7b	8b	18, 20	Return
	Screen	9				
2	1	1	5a	7a	5	CH5-
		4	5b	8a	18, 20	Return
	2	2	1a	9a	6	CH6-
		5	1b	10a	18, 20	Return
	3	3	3a	10b	7	CH7-
		6	3b	9b	18, 20	Return
	4	7	7a	7b	8	CH8-
		8	7b	8b	18, 20	Return
	Screen	9				
3	1	1	5a	7a	9	CH9-
		4	5b	8a	18, 20	Return
	2	2	1a	9a	10	CH10-
		5	1b	10a	18, 20	Return
	3	3	3a	10b	11	CH11-
		6	3b	9b	18, 20	Return
	4	7	7a	7b	12	CH12-
		8	7b	8b	18, 20	Return
	Screen	9				
4	1	1	5a	7a	13	CH13-
		4	5b	8a	18, 20	Return
	2	2	1a	9a	14	CH14-
		5	1b	10a	18, 20	Return
	3	3	3a	10b	15	CH15-
		6	3b	9b	18, 20	Return
	4	7	7a	7b	16	CH16-
		8	7b	8b	18, 20	Return
	Screen	9				
					17, 19	Fault

Terminal pin-out		
Power supply	X20	1+
		2-
	X20	3+
		4-
Fault	X20	5
		6

Module pin-out (SL1): module 1 ... 4	
V _{cc}	2a
	2b
GND	1a
	1b
FAULT	6b



The loop drawing has to be observed. For information see corresponding data sheet on www.pepperl-fuchs.com.