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Aggiungi valore.

OFFICIAL EXTERNAL REPORT

R-IS-220229-01-REV.2-EX-SUM

SIL Verification report- Executive Summary

Analysis of module HiC2025HC - KCD2-STC-EX1.HC(.SP)

Smart Transmitter Power Supply

Analysis of module HIC2031HC - KCD2-SCD-EX1.HC(.SP)

Smart Current Driver

Date June, 25 th 2012		Place Sesto San Giovanni
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Under the assumptions included in full report R-IS-220229-01 Rev.2, the calculations results fulfill all the architectural and the quantitative requirements of the maximum applicable SIL, according to the European Standard IEC EN 61508:2001 as in the following table:

Item name	System type	PFH [h-1]	PFDavg	Proof test time (T _{Proof})	Configuration	Allowed SIL	Notes
HiC2025HC KCD2-STC-Ex1.HC(.SP)	Type A	5,03 E-08	2,20 E-04	1 year	Input from active or passive Tx and	SIL2	Considering it not contributes more than 10% of total SIF dangerous failure
			4,41 E-04	2 years		SIL2	
			1,10 E-03	5 years	Output as current source or current sink or voltage source	SIL2	Considering it contributes more than 10% of total SIF dangerous failure
HiC2031HC KCD2-SCD-Ex1.HC(.SP)	Type A	6,59 E-08	2,89 E-04	1 year	Na	SIL2	Considering it not contribute more than 10% of total SIF dangerous failure
			5,77 E-04	2 years		SIL2	
			1,44 E-03	5 years		SIL2	Considering it contributes more than 10% of total SIF dangerous failure

Item name	Safe Detected Failure λSD	Safe Undetected Failure λSU	Dangerous Detected Failure λDD	Dangerous Undetected Failure λDU	Safe Failure Fraction SFF
HiC2025HC / KCD2-STC-Ex1.HC(.SP)	126,30 FIT	228,29 FIT	0,00 FIT	50,31 FIT	87,58 %
HiC2031HC / KCD2-SCD-Ex1.HC(.SP)	0,00 FIT	323,42 FIT	0,00 FIT	65,88 FIT	83,08 %

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