

may use the mark:

The manufacturer

Revision 4.0 October 28, 2022 Surveillance Audit Due October 31, 2025



Certificate / Certificat Zertifikat / 合格証

P+F 090535 P0006 C011

exida hereby confirms that the:

Functional Safety Management System of Pepperl + Fuchs SE

as implemented by:
Process Automation - Product Group Interface;
Mannheim, Germany

Has been assessed per the relevant requirements of:

IEC 61508: 2000 Parts 1 - 2 and

IEC 61508: 2010 Parts 1 - 2

and meets requirements providing a level of integrity to:

Systematic Capability: SC 3 (SIL 3 Capable)

Scope

The scope of this certification is defined by IEC 61508 part 1 - 2, phase 9 – 10 (for IEC 61508:2010) of the Overall Safety Lifecycle including the realisation of E/E/PE Safety Related Hardware Systems. A documented audit of the Functional Safety Management System confirmed the compliance with the relevant requirements.

Restrictions

This FSM certification only refers to the product engineering process as implemented in the process landscape of the Pepperl+Fuchs group for Mannheim, Germany. This FSM certification does not make any statement of the IEC 61508 compliance for any product of Pepperl + Fuchs SE. The development process of a specific product shall be audited to ensure compliance with this certified FSM process.



Evaluating Assessor

Certifying Assessor

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Systematic Capability: SC 3 (SIL 3 Capable)

Functional Safety Management System

Systematic Capability:

The Functional Safety Management system has met the requirements of Safety Integrity Level (SIL) 3. These are intended to achieve sufficient integrity against systematic errors of design by the manufacturer.

The following IEC 61508 objectives were subject to detailed auditing at Pepperl + Fuchs SE:

- FSM planning, including:
 - Safety Life Cycle definition
 - Scope of the FSM activities
 - Documentation
 - Activities and Responsibilities (Training and competence)
 - Configuration management
 - Tools
- Safety Requirement Specification
- · Change and modification management
- Hardware architecture design process, techniques and documentation
- Hardware design / probabilistic properties
- Hardware and system related V&V activities including documentation, verification
 - Integration and fault insertion test strategy
- System related V&V activities including documentation, verification
- System Validation Hardware
- Hardware-related operation, installation and maintenance requirements

The following documents are a mandatory part of certification:

Assessment Report: P+F 09/05-35-C R038 V4 R0



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