

## Pin-Out Table

Termination Board field side		Modules			Termination Board control side	Yokogawa signal name
Module	Channel	IS terminals TB1/TB2	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector CN1, CN2	SDV144
1	1	1	5a	8a	48	DC1
		4	5b	7a	47	IN1
		2	1a	10a		n.c.
		5	1b	9a		n.c.
2	2	1	5a	8a	46	DC2
		4	5b	7a	45	IN2
		2	1a	10a		n.c.
		5	1b	9a		n.c.
3	3	1	5a	8a	44	DC3
		4	5b	7a	43	IN3
		2	1a	10a		n.c.
		5	1b	9a		n.c.
4	4	1	5a	8a	42	DC4
		4	5b	7a	41	IN4
		2	1a	10a		n.c.
		5	1b	9a		n.c.
5	5	1	5a	8a	40	DC5
		4	5b	7a	39	IN5
		2	1a	10a		n.c.
		5	1b	9a		n.c.
6	6	1	5a	8a	38	DC6
		4	5b	7a	37	IN6
		2	1a	10a		n.c.
		5	1b	9a		n.c.
7	7	1	5a	8a	36	DC7
		4	5b	7a	35	IN7
		2	1a	10a		n.c.
		5	1b	9a		n.c.
8	8	1	5a	8a	34	DC8
		4	5b	7a	33	IN8
		2	1a	10a		n.c.
		5	1b	9a		n.c.
						n.c.

07/2015

TDOCT-2576BENG 236181

Refer to "General Notes Relating to Pepperl+Fuchs Product Information".

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Module	Channel	IS terminals TB1/TB2	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector CN1, CN2	SDV144
9	9	1	5a	8a	32	DC9
		4	5b	7a	31	IN9
		2	1a	10a		n.c.
		5	1b	9a		n.c.
10	10	1	5a	8a	30	DC10
		4	5b	7a	29	IN10
		2	1a	10a		n.c.
		5	1b	9a		n.c.
11	11	1	5a	8a	28	DC11
		4	5b	7a	27	IN11
		2	1a	10a		n.c.
		5	1b	9a		n.c.
12	12	1	5a	8a	26	DC12
		4	5b	7a	25	IN12
		2	1a	10a		n.c.
		5	1b	9a		n.c.
13	13	1	5a	8a	24	DC13
		4	5b	7a	23	IN13
		2	1a	10a		n.c.
		5	1b	9a		n.c.
14	14	1	5a	8a	22	DC14
		4	5b	7a	21	IN14
		2	1a	10a		n.c.
		5	1b	9a		n.c.
15	15	1	5a	8a	20	DC15
		4	5b	7a	19	IN15
		2	1a	10a		n.c.
		5	1b	9a		n.c.
16	16	1	5a	8a	18	DC16
		4	5b	7a	17	IN16
		2	1a	10a		n.c.
		5	1b	9a		n.c.
					9, 11 ... 16, 49	COM
					1, 50	CBSE
					2 ... 8, 10	24 V

## Terminal pin-out

Power supply	X20	3+
		4-
	X20	5+
		6-
Potential-free fault indication output	X20	1, 2

## Module pin-out (SL1): module 1 ... 16

V <sub>cc</sub>	2a (+)
	2b (+)
GND	1a (-)
	1b (-)
Fault	6b



The loop drawing has to be observed. For information see corresponding data sheet on [www.pepperl-fuchs.com](http://www.pepperl-fuchs.com).