

## Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Yokogawa signal name
Module	Channel	IS terminals TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector CN1, CN2	SDV144
1	1	1	5a	8a	48	DC1
		4	5b	7a	47	IN1
	2	2	1a	10a	46	DC2
		5	1b	9a	45	IN2
2	3	1	5a	8a	44	DC3
		4	5b	7a	43	IN3
	4	2	1a	10a	42	DC4
		5	1b	9a	41	IN4
3	5	1	5a	8a	40	DC5
		4	5b	7a	39	IN5
	6	2	1a	10a	38	DC6
		5	1b	9a	37	IN6
4	7	1	5a	8a	36	DC7
		4	5b	7a	35	IN7
	8	2	1a	10a	34	DC8
		5	1b	9a	33	IN8
5	9	1	5a	8a	32	DC9
		4	5b	7a	31	IN9
	10	2	1a	10a	30	DC10
		5	1b	9a	29	IN10
6	11	1	5a	8a	28	DC11
		4	5b	7a	27	IN11
	12	2	1a	10a	26	DC12
		5	1b	9a	25	IN12
7	13	1	5a	8a	24	DC13
		4	5b	7a	23	IN13
	14	2	1a	10a	22	DC14
		5	1b	9a	21	IN14
8	15	1	5a	8a	20	DC15
		4	5b	7a	19	IN15
	16	2	1a	10a	18	DC16
		5	1b	9a	17	IN16
					9, 11 ... 16, 49	COM
					1, 50	CBSE
					2 ... 8, 10	24 V

## Terminal pin-out

Power supply	X20	3+
		4-
	X20	5+
		6-
Potential-free fault indication output	X20	1, 2

## Module pin-out (SL1): module 1 ... 8

V <sub>cc</sub>	2a (+)
	2b (+)
GND	1a (-)
	1b (-)
Fault	6b



The loop drawing has to be observed. For information see corresponding data sheet on [www.pepperl-fuchs.com](http://www.pepperl-fuchs.com).