Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Triconex signal name
Module	Channel	IS terminal TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	3564
1	1	1	5a			
		4	5b	7a	AA	IN1
	2	2	1a			
		5	1b	9a	LL	IN2
	3	3	3a			
		6	3b	10b	z1	IN3
	4	7	7a			
		8	7b	7b	EE	IN4
2	5	1	5a			
		4	5b	7a	p1	IN5
	6	2	1a			
		5	1b	9a	v1	IN6
	7	3	3a			
		6	3b	10b	h1	IN7
	8	7	7a			
		8	7b	7b	l1	IN8
3	9	1	5a			
		4	5b	7a	e1	IN9
	10	2	1a			
		5	1b	9a	b1	IN10
	11	3	3a			
		6	3b	10b	w	IN11
	12	7	7a			
		8	7b	7b	S	IN12
4	13	1	5a			
		4	5b	7a	L	IN13
	14	2	1a			
		5	1b	9a	F	IN14
	15	3	3a			
		6	3b	10b	M	IN15
	16	7	7a			
		8	7b	7b	B	IN16
5	17	1	5a			
		4	5b	7a	BB	IN17
	18	2	1a			
		5	1b	9a	MM	IN18
	19	3	3a			
		6	3b	10b	CC	IN19
	20	7	7a			
		8	7b	7b	HH	IN20
6	21	1	5a			
		4	5b	7a	t1	IN21
	22	2	1a			
		5	1b	9a	x1	IN22
	23	3	3a			
		6	3b	10b	j1	IN23
	24	7	7a			
		8	7b	7b	m1	IN24
7	25	1	5a			
		4	5b	7a	f1	IN25
	26	2	1a			
		5	1b	9a	c1	IN26
	27	3	3a			
		6	3b	10b	Z	IN27
	28	7	7a			
		8	7b	7b	U	IN28

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Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Triconex signal name
Module	Channel	IS terminal TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	3564
8	29	1	5a			
		4	5b	7a	P	IN29
	30	2	1a			
		5	1b	9a	J	IN30
	31	3	3a			
		6	3b	10b	N	IN31
	32	7	7a			
		8	7b	7b	C	IN32
					A	RTN
					D	
					E	
					K	
					V	
					d1	
					a1	
					n1	
					y1	
					JJ	
					NN	
					u1	
					KK	

Terminal pin-out		
Power supply	X20	1+
		1-
	X20	2+
		2-
ERR	X20	Fault Bus

Module pin-out (SL1): module 1 ... 8	
V_{cc}	2a
	2b
	8a
	10a
	9b
	8b
GND	1a
	1b
ERR	6b

The loop drawing has to be observed. For information see corresponding data sheet on www.pepperl-fuchs.com.

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