

## Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Triconex signal name
Module	Channel	IS terminals TB1/TB2	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	3700A, 3721
1	1	1	5a	8a	AA	IN1+
		4	5b			
		2	1a	7a	LL	IN1-
		5	1b			
2	2	1	5a	8a	z1	IN2+
		4	5b			
		2	1a	7a	EE	IN2-
		5	1b			
3	3	1	5a	8a	p1	IN3+
		4	5b			
		2	1a	7a	v1	IN3-
		5	1b			
4	4	1	5a	8a	h1	IN4+
		4	5b			
		2	1a	7a	l1	IN4-
		5	1b			
5	5	1	5a	8a	e1	IN5+
		4	5b			
		2	1a	7a	b1	IN5-
		5	1b			
6	6	1	5a	8a	W	IN6+
		4	5b			
		2	1a	7a	S	IN6-
		5	1b			
7	7	1	5a	8a	L	IN7+
		4	5b			
		2	1a	7a	F	IN7-
		5	1b			
8	8	1	5a	8a	M	IN8+
		4	5b			
		2	1a	7a	B	IN8-
		5	1b			
9	9	1	5a	8a	BB	IN9+
		4	5b			
		2	1a	7a	MM	IN9-
		5	1b			
10	10	1	5a	8a	CC	IN10+
		4	5b			
		2	1a	7a	HH	IN10-
		5	1b			
11	11	1	5a	8a	t1	IN11+
		4	5b			
		2	1a	7a	x1	IN11-
		5	1b			
12	12	1	5a	8a	j1	IN12+
		4	5b			
		2	1a	7a	m1	IN12-
		5	1b			
13	13	1	5a	8a	f1	IN13+
		4	5b			
		2	1a	7a	c1	IN13-
		5	1b			
14	14	1	5a	8a	Z	IN14+
		4	5b			
		2	1a	7a	U	IN14-
		5	1b			

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Module	Channel	IS terminals TB1/TB2	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	3700A, 3721
15	15	1	5a	8a	P	IN15+
		4	5b			
		2	1a	7a	J	IN15-
		5	1b			
16	16	1	5a	8a	N	IN16+
		4	5b			
		2	1a	7a	C	IN16-
		5	1b			

Terminal pin-out	HART connector	Pin	Channel
HART	X3	1	IN1+
		2	IN1-
		3	IN2+
		4	IN2-
		5	IN3+
		6	IN3-
		7	IN4+
		8	IN4-
		9	IN5+
		10	IN5-
		11	IN6+
		12	IN6-
		13	IN7+
		14	IN7-
		15	IN8+
		16	IN8-
		17	IN9+
		18	IN9-
		19	IN10+
		20	IN10-
		21	IN11+
		22	IN11-
		23	IN12+
		24	IN12-
		25	IN13+
		26	IN13-
		27	IN14+
		28	IN14-
		29	IN15+
		30	IN15-
		31	IN16+
		32	IN16-

Terminal pin-out		
Power supply	X20	1+
		1-
	X20	2+
		2-
ERR	X20	Fault Bus

Module pin-out (SL1): module 1 ... 16	
V <sub>cc</sub>	2a
	2b
GND	1a
	1b
ERR	6b



The loop drawing has to be observed. For information see corresponding data sheet on [www.pepperl-fuchs.com](http://www.pepperl-fuchs.com).