

## Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Triconex signal name
Module	Channel	IS terminals TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	3805E
1	1	1	5a	8a	AA	OUT1
		4	5b	7a	LL	RTN1
2	2	1	5a	8a	z1	OUT2
		4	5b	7a	EE	RTN2
3	3	1	5a	8a	p1	OUT3
		4	5b	7a	v1	RTN3
4	4	1	5a	8a	h1	OUT4
		4	5b	7a	l1	RTN4
5	5	1	5a	8a	e1	OUT5
		4	5b	7a	b1	RTN5
6	6	1	5a	8a	W	OUT6
		4	5b	7a	S	RTN6
7	7	1	5a	8a	L	OUT7
		4	5b	7a	F	RTN7
8	8	1	5a	8a	M	OUT8
		4	5b	7a	B	RTN8
					x1, m1	RTN
					t1	PWR1
					j1	PWR2

Terminal pin-out	HART connector	Pin	Channel
HART	X3	1	OUT1
		2	RTN1
		3	OUT2
		4	RTN2
		5	OUT3
		6	RTN3
		7	OUT4
		8	RTN4
		9	OUT5
		10	RTN5
		11	OUT6
		12	RTN6
		13	OUT7
		14	RTN7
		15	OUT8
		16	RTN8

Terminal pin-out		
Power supply	X20	1+
		1-
	X20	2+
		2-
ERR	X20	Fault Bus

Module pin-out (SL1): module 1 ... 8	
V <sub>cc</sub>	2a
	2b
GND	1a
	1b
ERR	6b



The loop drawing has to be observed. For information see corresponding data sheet on [www.pepperl-fuchs.com](http://www.pepperl-fuchs.com).