

## Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Yokogawa signal name	
Module	Channel	IS terminals TB1/TB2	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector CN1, CN2	ADV551	
1	1	1	5a	8b, V+ 1 ... 16	50	OUT1	
		4	5b	7b			
		7	7a				
	2	2	2	1a	9b, V+ 1 ... 16	48	OUT2
			5	1b	10b		
			6	3b			
2	3	1	5a	8b, V+ 1 ... 16	46	OUT3	
		4	5b	7b			
		7	7a				
	4	4	2	1a	9b, V+ 1 ... 16	44	OUT4
			5	1b	10b		
			6	3b			
3	5	1	5a	8b, V+ 1 ... 16	42	OUT5	
		4	5b	7b			
		7	7a				
	6	6	2	1a	9b, V+ 1 ... 16	40	OUT6
			5	1b	10b		
			6	3b			
4	7	1	5a	8b, V+ 1 ... 16	38	OUT7	
		4	5b	7b			
		7	7a				
	8	8	2	1a	9b, V+ 1 ... 16	36	OUT8
			5	1b	10b		
			6	3b			
5	9	1	5a	8b, V+ 1 ... 16	34	OUT9	
		4	5b	7b			
		7	7a				
	10	10	2	1a	9b, V+ 1 ... 16	32	OUT10
			5	1b	10b		
			6	3b			
6	11	1	5a	8b, V+ 1 ... 16	30	OUT11	
		4	5b	7b			
		7	7a				
	12	12	2	1a	9b, V+ 1 ... 16	28	OUT12
			5	1b	10b		
			6	3b			
7	13	1	5a	8b, V+ 1 ... 16	26	OUT13	
		4	5b	7b			
		7	7a				
	14	14	2	1a	9b, V+ 1 ... 16	24	OUT14
			5	1b	10b		
			6	3b			
8	15	1	5a	8b, V+ 1 ... 16	22	OUT15	
		4	5b	7b			
		7	7a				
	16	16	2	1a	9b, V+ 1 ... 16	20	OUT16
			5	1b	10b		
			6	3b			
9	17	1	5a	8b, V+ 17 ... 32	49	OUT17	
		4	5b	7b			
		7	7a				
	18	18	2	1a	9b, V+ 17 ... 32	47	OUT18
			5	1b	10b		
			6	3b			
10	19	1	5a	8b, V+ 17 ... 32	45	OUT19	
		4	5b	7b			
		7	7a				
	20	20	2	1a	9b, V+ 17 ... 32	43	OUT20
			5	1b	10b		
			6	3b			

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Refer to "General Notes Relating to Pepperl+Fuchs Product Information".

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Module	Channel	IS terminals TB1/TB2	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector CN1, CN2	ADV551
11	21	1	5a	8b, V+ 17 ... 32		
		4	5b	7b	41	OUT21
		7	7a			
	22	2	1a	9b, V+ 17 ... 32		
		5	1b	10b	39	OUT22
		6	3b			
12	23	1	5a	8b, V+ 17 ... 32		
		4	5b	7b	37	OUT23
		7	7a			
	24	2	1a	9b, V+ 17 ... 32		
		5	1b	10b	35	OUT24
		6	3b			
13	25	1	5a	8b, V+ 17 ... 32		
		4	5b	7b	33	OUT25
		7	7a			
	26	2	1a	9b, V+ 17 ... 32		
		5	1b	10b	31	OUT26
		6	3b			
14	27	1	5a	8b, V+ 17 ... 32		
		4	5b	7b	29	OUT27
		7	7a			
	28	2	1a	9b, V+ 17 ... 32		
		5	1b	10b	27	OUT28
		6	3b			
15	29	1	5a	8b, V+ 17 ... 32		
		4	5b	7b	25	OUT29
		7	7a			
	30	2	1a	9b, V+ 17 ... 32		
		5	1b	10b	23	OUT30
		6	3b			
16	31	1	5a	8b, V+ 17 ... 32		
		4	5b	7b	21	OUT31
		7	7a			
	32	2	1a	9b, V+ 17 ... 32		
		5	1b	10b	19	OUT32
		6	3b			
					1, 2	CBSE
					3, 4, 5, 6	n.c.
					7, 9	V+ 17 ... 32
					8, 10	V+ 1 ... 16
					11 ... 18	COM

Terminal pin-out		
Power supply	X20	3+
		4-
	X20	5+
		6-
Potential-free fault indication output	X20	1, 2

Module pin-out (SL1): module 1 ... 16	
V <sub>cc</sub>	2a (+)
	2b (+)
GND	1a (-)
	1b (-)
Fault	6b



The loop drawing has to be observed. For information see corresponding data sheet on [www.pepperl-fuchs.com](http://www.pepperl-fuchs.com).