Pin-Out Table

Termination Board field side			Modules		Termination Board control side	Emerson signal name
Module	Channel	IS terminals TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector CN1	DeltaV
1	1	1	5a	7a	18, 20	Return
		4	5b	8a	1	CH1+
	2	2	1a	9a	18, 20	Return
		5	1b	10a	2	CH2+
	3	3	3a	10b	18, 20	Return
		6	3b	9b	3	CH3+
	4	7	7a	7b	18, 20	Return
		8	7b	8b	4	CH4+
	Screen	9				
2	1	1	5a	7a	18, 20	Return
		4	5b	8a	5	CH5+
	2	2	1a	9a	18, 20	Return
		5	1b	10a	6	CH6+
	3	3	3a	10b	18, 20	Return
		6	3b	9b	7	CH7+
	4	7	7a	7b	18, 20	Return
		8	7b	8b	8	CH8+
	Screen	9				
3	1	1	5a	7a	18, 20	Return
		4	5b	8a	9	CH9+
	2	2	1a	9a	18, 20	Return
		5	1b	10a	10	CH10+
	3	3	3a	10b	18, 20	Return
		6	3b	9b	11	CH11+
	4	7	7a	7b	18, 20	Return
		8	7b	8b	12	CH12+
	Screen	9				
4	1	1	5a	7a	18, 20	Return
		4	5b	8a	13	CH13+
	2	2	1a	9a	18, 20	Return
		5	1b	10a	14	CH14+
	3	3	3a	10b	18, 20	Return
		6	3b	9b	15	CH15+
	4	7	7a	7b	18, 20	Return
		8	7b	8b	16	CH16+
	Screen	9				

Terminal pin-out		
Power supply	X20	1+
		2-
	X20	3+
		4-
Fault	X20	5
		6

Module pin-out (SL1): module 1 ... 4	
V_{cc}	2a
	2b
GND	1a
	1b
FAULT	6b

The loop drawing has to be observed. For information see corresponding data sheet on www.pepperl-fuchs.com.

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