

Pin-Out Table

Termination Board field side			Modules		Termination Board control side	
Module	Channel	IS terminals M1 ... M8	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	Signal name
1	1	1	5a	8a	37	CH1+
		4	5b	7a	19	CH1-
	2	2	1a	10a	36	CH2+
		5	1b	9a	18	CH2-
2	3	1	5a	8a	35	CH3+
		4	5b	7a	17	CH3-
	4	2	1a	10a	34	CH4+
		5	1b	9a	16	CH4-
3	5	1	5a	8a	33	CH5+
		4	5b	7a	15	CH5-
	6	2	1a	10a	32	CH6+
		5	1b	9a	14	CH6-
4	7	1	5a	8a	31	CH7+
		4	5b	7a	13	CH7-
	8	2	1a	10a	30	CH8+
		5	1b	9a	12	CH8-
5	9	1	5a	8a	29	CH9+
		4	5b	7a	11	CH9-
	10	2	1a	10a	28	CH10+
		5	1b	9a	10	CH10-
6	11	1	5a	8a	27	CH11+
		4	5b	7a	9	CH11-
	12	2	1a	10a	26	CH12+
		5	1b	9a	8	CH12-
7	13	1	5a	8a	25	CH13+
		4	5b	7a	7	CH13-
	14	2	1a	10a	24	CH14+
		5	1b	9a	6	CH14-
8	15	1	5a	8a	23	CH15+
		4	5b	7a	5	CH15-
	16	2	1a	10a	22	CH16+
		5	1b	9a	4	CH16-
					1	V _{cc}
					20	GND

Pin-Out Table


Termination Board field side			Modules		Termination Board control side	
Module	Channel	IS terminals M9 ... M16	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X2	Signal name
9	17	1	5a	8a	37	CH17+
		4	5b	7a	19	CH17-
	18	2	1a	10a	36	CH18+
		5	1b	9a	18	CH18-
10	19	1	5a	8a	35	CH19+
		4	5b	7a	17	CH19-
	20	2	1a	10a	34	CH20+
		5	1b	9a	16	CH20-
11	21	1	5a	8a	33	CH21+
		4	5b	7a	15	CH21-
	22	2	1a	10a	32	CH22+
		5	1b	9a	14	CH22-
12	23	1	5a	8a	31	CH23+
		4	5b	7a	13	CH23-
	24	2	1a	10a	30	CH24+
		5	1b	9a	12	CH24-
13	25	1	5a	8a	29	CH25+
		4	5b	7a	11	CH25-
	26	2	1a	10a	28	CH26+
		5	1b	9a	10	CH26-
14	27	1	5a	8a	27	CH27+
		4	5b	7a	9	CH27-
	28	2	1a	10a	26	CH28+
		5	1b	9a	8	CH28-
15	29	1	5a	8a	25	CH29+
		4	5b	7a	7	CH29-
	30	2	1a	10a	24	CH30+
		5	1b	9a	6	CH30-
16	31	1	5a	8a	23	CH31+
		4	5b	7a	5	CH31-
	32	2	1a	10a	22	CH32+
		5	1b	9a	4	CH32-
					1	V _{cc}
					20	GND

Pin-Out Table

Terminal pin-out	Connector	Pin	Channel
HART Signal	HART1	1, 2	CH1+, CH1-
		3, 4	CH2+, CH2-
		5, 6	CH3+, CH3-
		7, 8	CH4+, CH4-
		9, 10	CH5+, CH5-
		11, 12	CH6+, CH6-
		13, 14	CH7+, CH7-
		15, 16	CH8+, CH8-
		17, 18	CH9+, CH9-
		19, 20	CH10+, CH10-
		21, 22	CH11+, CH11-
		23, 24	CH12+, CH12-
		25, 26	CH13+, CH13-
		27, 28	CH14+, CH14-
		29, 30	CH15+, CH15-
		31, 32	CH16+, CH16-
	HART2	1, 2	CH17+, CH17-
		3, 4	CH18+, CH18-
		5, 6	CH19+, CH19-
		7, 8	CH20+, CH20-
		9, 10	CH21+, CH21-
		11, 12	CH22+, CH22-
		13, 14	CH23+, CH23-
		15, 16	CH24+, CH24-
		17, 18	CH25+, CH25-
		19, 20	CH26+, CH26-
		21, 22	CH27+, CH27-
		23, 24	CH28+, CH28-
		25, 26	CH29+, CH29-
		27, 28	CH30+, CH30-
		29, 30	CH31+, CH31-
		31, 32	CH32+, CH32-

Terminal pin-out	Connector	Pin	Signal name
Power supply	X20	6	Supply I -
		5	Supply I +
	X20	4	Supply II -
		3	Supply II +
Fault	X20	1, 2	Fault

Module pin-out (SL1): module 1 ... 16	
V _{cc}	2a (+)
	2b (+)
GND	1a (-)
	1b (-)
Fault	6b

 The loop drawing has to be observed. For information see corresponding data sheet on www.pepperl-fuchs.com.