

## Pin-Out Table

Termination Board field side			Modules		Termination Board control side	
Module	Channel	IS terminals M1 ... M32	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector XG1	HIMA signal name X-DI 32 01 X-DI 32 04
1	1	1	5a	8a	a1	S1+
		4	5b			
		2	1a	7a	b1	DI1+
		5	1b			
2	2	1	5a	8a	a1	S1+
		4	5b			
		2	1a	7a	b2	DI2+
		5	1b			
3	3	1	5a	8a	a1	S1+
		4	5b			
		2	1a	7a	b3	DI3+
		5	1b			
4	4	1	5a	8a	a1	S1+
		4	5b			
		2	1a	7a	b4	DI4+
		5	1b			
5	5	1	5a	8a	a2	S2+
		4	5b			
		2	1a	7a	b5	DI5+
		5	1b			
6	6	1	5a	8a	a2	S2+
		4	5b			
		2	1a	7a	b6	DI6+
		5	1b			
7	7	1	5a	8a	a2	S2+
		4	5b			
		2	1a	7a	b7	DI7+
		5	1b			
8	8	1	5a	8a	a2	S2+
		4	5b			
		2	1a	7a	b8	DI8+
		5	1b			
9	9	1	5a	8a	a3	S3+
		4	5b			
		2	1a	7a	b9	DI9+
		5	1b			
10	10	1	5a	8a	a3	S3+
		4	5b			
		2	1a	7a	b10	DI10+
		5	1b			
11	11	1	5a	8a	a3	S3+
		4	5b			
		2	1a	7a	b11	DI11+
		5	1b			
12	12	1	5a	8a	a3	S3+
		4	5b			
		2	1a	7a	b12	DI12+
		5	1b			
13	13	1	5a	8a	a4	S4+
		4	5b			
		2	1a	7a	b13	DI13+
		5	1b			
14	14	1	5a	8a	a4	S4+
		4	5b			
		2	1a	7a	b14	DI14+
		5	1b			
15	15	1	5a	8a	a4	S4+
		4	5b			
		2	1a	7a	b15	DI15+
		5	1b			

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Refer to "General Notes Relating to Pepperl+Fuchs Product Information".

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Module	Channel	IS terminals M1 ... M32	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector XG1	HIMA signal name X-DI 32 01 X-DI 32 04
16	16	1	5a	8a	a4	S4+
		4	5b			
		2	1a	7a	b16	DI16+
		5	1b			
17	17	1	5a	8a	a5	S5+
		4	5b			
		2	1a	7a	b17	DI17+
		5	1b			
18	18	1	5a	8a	a5	S5+
		4	5b			
		2	1a	7a	b18	DI18+
		5	1b			
19	19	1	5a	8a	a5	S5+
		4	5b			
		2	1a	7a	b19	DI19+
		5	1b			
20	20	1	5a	8a	a5	S5+
		4	5b			
		2	1a	7a	b20	DI20+
		5	1b			
21	21	1	5a	8a	a6	S6+
		4	5b			
		2	1a	7a	b21	DI21+
		5	1b			
22	22	1	5a	8a	a6	S6+
		4	5b			
		2	1a	7a	b22	DI22+
		5	1b			
23	23	1	5a	8a	a6	S6+
		4	5b			
		2	1a	7a	b23	DI23+
		5	1b			
24	24	1	5a	8a	a6	S6+
		4	5b			
		2	1a	7a	b24	DI24+
		5	1b			
25	25	1	5a	8a	a7	S7+
		4	5b			
		2	1a	7a	b25	DI25+
		5	1b			
26	26	1	5a	8a	a7	S7+
		4	5b			
		2	1a	7a	b26	DI26+
		5	1b			
27	27	1	5a	8a	a7	S7+
		4	5b			
		2	1a	7a	b27	DI27+
		5	1b			
28	28	1	5a	8a	a7	S7+
		4	5b			
		2	1a	7a	b28	DI28+
		5	1b			
29	29	1	5a	8a	a8	S8+
		4	5b			
		2	1a	7a	b29	DI29+
		5	1b			
30	30	1	5a	8a	a8	S8+
		4	5b			
		2	1a	7a	b30	DI30+
		5	1b			

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Module	Channel	IS terminals M1 ... M32	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector XG1	HIMA signal name X-DI 32 01 X-DI 32 04
31	31	1	5a	8a	a8	S8+
		4	5b			
		2	1a	7a	b31	DI31+
		5	1b			
32	32	1	5a	8a	a8	S8+
		4	5b			
		2	1a	7a	b32	DI32+
		5	1b			
					a32	reserved
					a31	reserved
					a30	reserved
					a29	reserved

Terminal pin-out	Connector	Pin	Signal name
Power supply	XG2	1	Supply I +
		2	Supply I -
	XG2	3	Supply II +
		4	Supply II -
Volt-free indication output	XG2	5, 6	Fault

Module pin-out (SL1): module 1 ... 32	
V <sub>cc</sub>	2a (+)
	2b (+)
GND	1a (-)
	1b (-)
Fault	6b

 The loop drawing has to be observed. For information see corresponding data sheet on [www.pepperl-fuchs.com](http://www.pepperl-fuchs.com).