

Pin-Out Table

Termination Board field side		Modules			Termination Board control side	HIMA signal name
Module	Channel	IS terminals M1 ... M32	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector XG1	X-AI 32 01/X-AI 32 02
1	1	1	5a	8a	b1	AI1+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
2	2	1	5a	8a	b2	AI2+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
3	3	1	5a	8a	b3	AI3+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
4	4	1	5a	8a	b4	AI4+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
5	5	1	5a	8a	b5	AI5+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
6	6	1	5a	8a	b6	AI6+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
7	7	1	5a	8a	b7	AI7+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
8	8	1	5a	8a	b8	AI8+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
9	9	1	5a	8a	b9	AI9+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
10	10	1	5a	8a	b10	AI10+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
11	11	1	5a	8a	b11	AI11+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
12	12	1	5a	8a	b12	AI12+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
13	13	1	5a	8a	b13	AI13+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
14	14	1	5a	8a	b14	AI14+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
15	15	1	5a	8a	b15	AI15+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			

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Refer to "General Notes Relating to Pepperl+Fuchs Product Information".

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Termination Board field side		Modules			Termination Board control side	HIMA signal name
Module	Channel	IS terminals M1 ... M32	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector XG1	X-AI 32 01/X-AI 32 02
16	16	1	5a	8a	b16	AI16+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
17	17	1	5a	8a	b17	AI17+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
18	18	1	5a	8a	b18	AI18+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
19	19	1	5a	8a	b19	AI19+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
20	20	1	5a	8a	b20	AI20+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
21	21	1	5a	8a	b21	AI21+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
22	22	1	5a	8a	b22	AI22+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
23	23	1	5a	8a	b23	AI23+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
24	24	1	5a	8a	b24	AI24+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
25	25	1	5a	8a	b25	AI25+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
26	26	1	5a	8a	b26	AI26+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
27	27	1	5a	8a	b27	AI27+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
28	28	1	5a	8a	b28	AI28+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
29	29	1	5a	8a	b29	AI29+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			
30	30	1	5a	8a	b30	AI30+
		4	5b			
		2	1a	7a	a1 - a8	AI-
		5	1b			

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Termination Board field side			Modules			Termination Board control side	HIMA signal name
Module	Channel	IS terminals M1 ... M32	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector XG1	X-AI 32 01/X-AI 32 02	
31	31	1	5a	8a	b31	AI31+	
		4	5b				
		2	1a	7a	a1 - a8	AI-	
		5	1b				
32	32	1	5a	8a	b32	AI32+	
		4	5b				
		2	1a	7a	a1 - a8	AI-	
		5	1b				
					a32	reserved	
					a31	reserved	
					a30	reserved	
					a29	reserved	

Terminal pin-out	Connector	Pin	Signal name	
HART signal	HART1	1, 2	AI1+, AI-	
		3, 4	AI2+, AI-	
		5, 6	AI3+, AI-	
		7, 8	AI4+, AI-	
		9, 10	AI5+, AI-	
		11, 12	AI6+, AI-	
		13, 14	AI7+, AI-	
		15, 16	AI8+, AI-	
		17, 18	AI9+, AI-	
		19, 20	AI10+, AI-	
		21, 22	AI11+, AI-	
		23, 24	AI12+, AI-	
		25, 26	AI13+, AI-	
		27, 28	AI14+, AI-	
		29, 30	AI15+, AI-	
		31, 32	AI16+, AI-	
		HART2	1, 2	AI17+, AI-
			3, 4	AI18+, AI-
			5, 6	AI19+, AI-
			7, 8	AI20+, AI-
			9, 10	AI21+, AI-
			11, 12	AI22+, AI-
			13, 14	AI23+, AI-
			15, 16	AI24+, AI-
			17, 18	AI25+, AI-
			19, 20	AI26+, AI-
			21, 22	AI27+, AI-
			23, 24	AI28+, AI-
			25, 26	AI29+, AI-
			27, 28	AI30+, AI-
			29, 30	AI31+, AI-
			31, 32	AI32+, AI-


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Pin-Out Table

Terminal pin-out	Connector	Pin	Signal name
Power supply	XG2	1	Supply I +
		2	Supply I -
	XG2	3	Supply II +
		4	Supply II -
Volt-free indication output	XG2	5, 6	Fault

Module pin-out (SL1): module 1 ... 32	
V _{cc}	2a (+)
	2b (+)
GND	1a (-)
	1b (-)
Fault	6b

 The loop drawing has to be observed. For information see corresponding data sheet on www.pepperl-fuchs.com.