

Pin-Out Table

Termination Board field side			Modules		Termination Board control side	
Module	Channel	IS terminals M1 ... M4	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	Signal name
1	1	1	5a	8a	37	CH1+
		4	5b			
	2	2	1a	7a	19	CH1-
		5	1b			
	3	3	3a	10a	36	CH2+
		6	3b			
	4	7	7a	9a	18	CH2-
		8	7b			
2	5	1	5a	8a	35	CH3+
		4	5b			
	6	2	1a	7a	17	CH3-
		5	1b			
	7	3	3a	10a	34	CH4+
		6	3b			
	8	7	7a	9a	16	CH4-
		8	7b			
3	9	1	5a	8a	33	CH5+
		4	5b			
	10	2	1a	7a	15	CH5-
		5	1b			
	11	3	3a	10a	32	CH6+
		6	3b			
	12	7	7a	9a	14	CH6-
		8	7b			
4	13	1	5a	8a	31	CH7+
		4	5b			
	14	2	1a	7a	13	CH7-
		5	1b			
	15	3	3a	10a	30	CH8+
		6	3b			
	16	7	7a	9a	12	CH8-
		8	7b			
					1	V _{cc}
					20	GND

Pin-Out Table

Termination Board field side			Modules		Termination Board control side	
Module	Channel	IS terminals M5 ... M8	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	Signal name
5	17	1	5a	8a	29	CH9+
		4	5b			
	18	2	1a	7a	11	CH9-
		5	1b			
	19	3	3a	10a	28	CH10+
		6	3b			
	20	7	7a	9a	10	CH10-
		8	7b			
6	21	1	5a	8a	27	CH11+
		4	5b			
	22	2	1a	7a	9	CH11-
		5	1b			
	23	3	3a	10a	26	CH12+
		6	3b			
	24	7	7a	9a	8	CH12-
		8	7b			
7	25	1	5a	8a	25	CH13+
		4	5b			
	26	2	1a	7a	7	CH13-
		5	1b			
	27	3	3a	10a	24	CH14+
		6	3b			
	28	7	7a	9a	6	CH14-
		8	7b			
8	29	1	5a	8a	23	CH15+
		4	5b			
	30	2	1a	7a	5	CH15-
		5	1b			
	31	3	3a	10a	22	CH16+
		6	3b			
	32	7	7a	9a	4	CH16-
		8	7b			
					1	V _{cc}
					20	GND

Pin-Out Table

Terminal pin-out	Connector	Pin	Channel
HART Signal	HART	1, 2	CH1+, CH1-
		3, 4	CH2+, CH2-
		5, 6	CH3+, CH3-
		7, 8	CH4+, CH4-
		9, 10	CH5+, CH5-
		11, 12	CH6+, CH6-
		13, 14	CH7+, CH7-
		15, 16	CH8+, CH8-
		17, 18	CH9+, CH9-
		19, 20	CH10+, CH10-
		21, 22	CH11+, CH11-
		23, 24	CH12+, CH12-
		25, 26	CH13+, CH13-
		27, 28	CH14+, CH14-
		29, 30	CH15+, CH15-
		31, 32	CH16+, CH16-

Terminal pin-out	Connector	Pin	Signal name
Power supply	X20	6	Supply I -
		5	Supply I +
	X20	4	Supply II -
		3	Supply II +
Fault	X20	1, 2	Fault

Module pin-out (SL1): module 1 ... 8	
V _{cc}	2a (+)
	2b (+)
GND	1a (-)
	1b (-)
Fault	6b



The loop drawing has to be observed. For information see corresponding data sheet on www.pepperl-fuchs.com.