

## Pinout Table

Termination board field side			Modules		Termination board control side	
Module	Channel	IS terminal TB1	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	System connector X2
1	1	1	5a	8a	A9	
		2	5b	7a	*	
		3	1a	10a		A1
		4	1b	9a		A9
2	2	5	5a	8a	B9	
		6	5b	7a	*	
		7	1a	10a		A1
		8	1b	9a		B9
3	3	9	5a	8a	A8	
		10	5b	7a	*	
		11	1a	10a		A1
		12	1b	9a		A8
4	4	13	5a	8a	B8	
		14	5b	7a	*	
		15	1a	10a		A1
		16	1b	9a		B8
5	5	17	5a	8a	A7	
		18	5b	7a	*	
		19	1a	10a		A1
		20	1b	9a		A7
6	6	21	5a	8a	B7	
		22	5b	7a	*	
		23	1a	10a		A1
		24	1b	9a		B7
7	7	25	5a	8a	A6	
		26	5b	7a	*	
		27	1a	10a		A1
		28	1b	9a		A6
8	8	29	5a	8a	B6	
		30	5b	7a	*	
		31	1a	10a		A1
		32	1b	9a		B6

## Pinout Table

Termination board field side			Modules		Termination board control side	
Module	Channel	IS terminal TB2	IS terminals SL2 field side	Non-IS terminals SL1 control side	System connector X1	System connector X2
9	9	1	5a	8a	A5	
		2	5b	7a	*	
		3	1a	10a		A1
		4	1b	9a		A5
10	10	5	5a	8a	B5	
		6	5b	7a	*	
		7	1a	10a		A1
		8	1b	9a		B5
11	11	9	5a	8a	A4	
		10	5b	7a	*	
		11	1a	10a		A1
		12	1b	9a		A4
12	12	13	5a	8a	B4	
		14	5b	7a	*	
		15	1a	10a		A1
		16	1b	9a		B4
13	13	17	5a	8a	A3	
		18	5b	7a	*	
		19	1a	10a		A1
		20	1b	9a		A3
14	14	21	5a	8a	B3	
		22	5b	7a	*	
		23	1a	10a		A1
		24	1b	9a		B3
15	15	25	5a	8a	A2	
		26	5b	7a	*	
		27	1a	10a		A1
		28	1b	9a		A2
16	16	29	5a	8a	B2	
		30	5b	7a	*	
		31	1a	10a		A1
		32	1b	9a		B2

\* Plug-in jumper in FS (X1: A10, B10)

\* Plug-in jumper in non-FS (X1: A1, B1)

Terminal pinout	Connector	Pin	Signal name
Power supply	TB3	1	Supply I -
		2	Supply I +
		3	Supply II -
		4	Supply II +
Volt-free fault indication output	TB4	1	Fault
		2	

Module pinout (SL1): module 1 ... 16	
Vcc	2a
	2b
GND	1a
	1b
Fault	6b

The typical circuit drawing has to be observed. For information see corresponding data sheet on [www.pepperl-fuchs.com](http://www.pepperl-fuchs.com).