

- 2-Channel
- Control circuit EEx ia IIC
- DC 24 V supply voltage
- Reversible operating mode
- Lead breakage (LB) and short circuit (SC) monitoring can be disabled
- Override input for each of the two transistor outputs per channel
- 1 active transistor output per channel, fault indication
- 3 active transistor outputs per channel

The transformer isolated amplifier transmits digital signals from the hazardous area. Sensors per DIN 19234 (NAMUR) or mechanical contacts can serve as inputs. The control circuit can be monitored by the lead breakage and short circuit monitoring.

### Notes on connection assignments: Inputs d30; z30 (LL+)

These inputs are internally connected. Supply for transistor outputs d32, z32.

### Lead breakage monitoring (LB) d6, d8; z6, z8

Lead breakage monitoring can be disabled by bridging the above connections.

### Mode of operation d16; z16

Logic-1: no reversal of operating mode from input to output

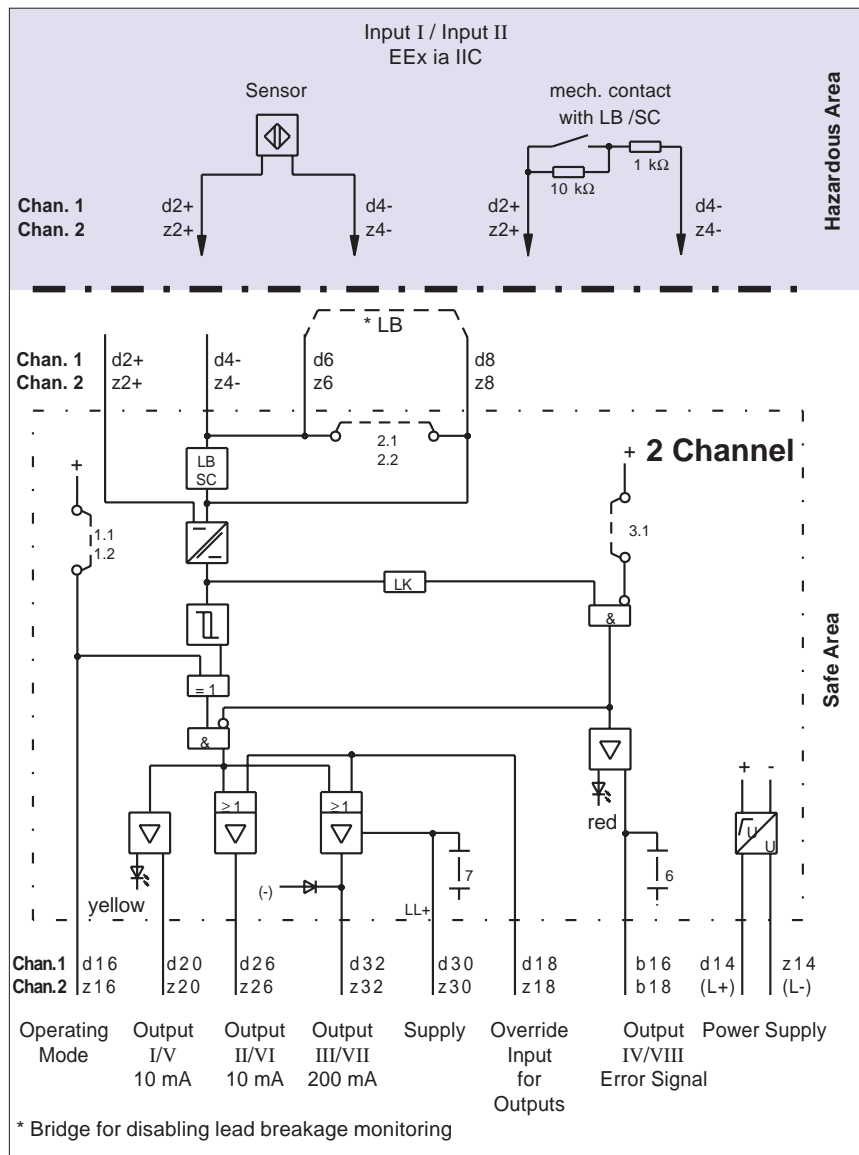
Logic-0: reversal of operating mode from input to output

(see table of operating modes on page 25)  
If necessary, the operating mode can also be selected with a factory installed jumper on the card.

### Fault signal outputs b16; b18

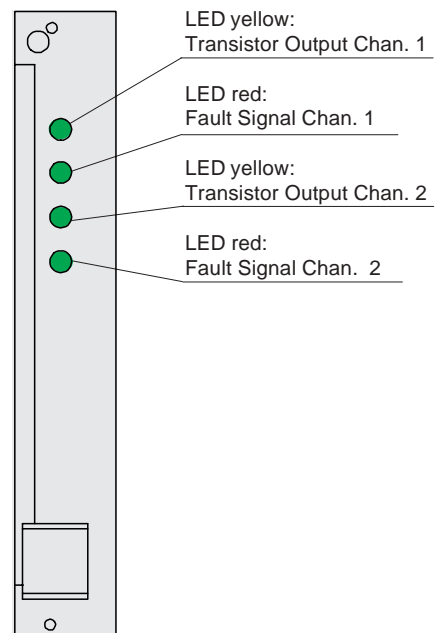
Short circuit or lead breakage monitoring (Indicator: LED red).

The fault signal outputs are combined by a circuit lead, so that only one connection is required for the fault evaluation of all channels on the card. If the evaluation of each individual channel is necessary, then the circuit lead must be breached at position 6 (see the side view of the card).



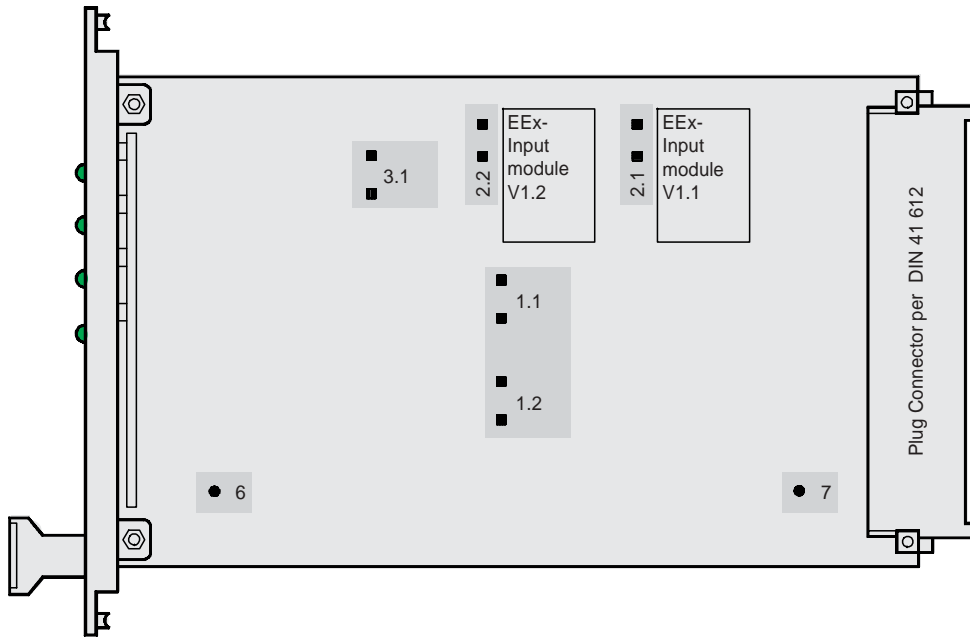
### Front View

Type A  
(dimensions see page 16)





Side View



**Programming facilities on the card**

**Jumpers**

Plug-in design optional  
 1.1 (open) Reverse operating mode channel 1  
 1.2 (open) Reverse operating mode channel 2  
 2.1 (open) Lead breakage monitoring channel 1: active  
 2.2 (open) Lead breakage monitoring channel 2: active  
 If bridge 3.1 is closed, short circuit and lead breakage monitoring for all channels are disabled

**Breach Points**

6 Separation of the error signal outputs  
 7 Separation of the common supply points LL+ for the transistor final power levels

**Delivery status**

Jumpers  
 1.1 to 3.1: open